

References

- [1] O'Boyle M.F.P, Ford R.W and Nisbet A.P., **A Compiler Algorithm to Reduce Invalidation Latency in Virtual Shared Memory Systems**, PACT '96, Parallel Architectures and Compiler Technology, *IEEE Press*, Boston, October 1996.
- [2] O'Boyle M.F.P. and Bull M., **Expert Programmer vs Automatic Parallelisation: Two approaches to Shared Virtual Memory**, Scientific Programming, Eds. Babb R.G. and Perrott R.H., *John Wiley and Sons Press.*, March 1996.
- [3] O'Boyle M.F.P, Ford R.W and Nisbet A.P., **Compiler Reduction of Invalidation Traffic In Virtual Shared Memory Systems**, EuroPar '96, *Springer-Verlag*, LNCS, Lyon, August 1996.
- [4] Sakellariou R. and O'Boyle M.F.P., **Towards a Parallelising COBOL Compiler** 14th IASTED Conference on Applied Informatics, *IASTED Press*, Innsbruck, February 1996.
- [5] Chamski Z. and O'Boyle M.F.P., **Practical Loop Generation** HICSS-29, Hawaii International Conference on System Sciences, *IEEE Press*, Hawaii, January 1996.
- [6] O'Boyle M.F.P., Kervella L. and Bodin F., **Synchronisation Minimisation in a SPMD Execution Model**, Journal of Parallel and Distributed Computing Vol 29. 196-210, *Academic Press Inc.*, September 1995.
- [7] O'Boyle M.F.P. and Bull M., **Loop versus Data Scheduling: Models, Language and Application for SVM**, Working Conference on Massively Parallel Programming Models, *IEEE Press*, Berlin, October 1995.
- [8] O'Boyle M.F.P. and Bodin F., **Compiler Reduction of Synchronisation in Shared Virtual Memory Systems**, ICS, 9th ACM International Conference on Supercomputing, *ACM Press*, Barcelona, July 1995.
- [9] Bodin F. and O'Boyle M.F.P., **A Compiler Strategy for SVM**, the Third Workshop on Languages, Compilers and Runtime Systems for Scalable Computing, *Kluwer Press*, New York, May 1995.
- [10] O'Boyle M.F.P., **A Hierarchical Locality Algorithm for NUMA Compilation**, EuroMicro Workshop on Parallel and Distributed Processing, *IEEE Press*, Italy, January 1995.
- [11] O'Boyle M.F.P., **A Data Partitioning Algorithm for Distributed Memory Compilation**, PARLE '94: Parallel Architectures and Languages Europe, LNCS 817 *Springer-Verlag*, Athens July 1994.
- [12] O'Boyle M.F.P. and Hedayat G.A., **A Transformational Approach to Compiling Sisal for Distributed Memory Architectures**, ICS, 6th ACM International Conference on Supercomputing, *ACM Press*, Washington, July 1992.
- [13] O'Boyle M.F.P. and Hedayat G.A., **A New Program Transformation To Minimise Communication in Distributed Memory Architectures**, PARLE '92: Parallel Architectures and Languages Europe, *Springer-Verlag*, LNCS 605, Paris, June 1992.

- [14] O'Boyle M.F.P. and Hedayat G.A., **Data Alignment: Transformation to Reduce Communication on Distributed Memory Architectures**, Scalable High Performance Computing Conference, *IEEE Press*, Virginia, April 1992.
- [15] O'Boyle M.F.P. and Hedayat G.A., **Load Balancing of Parallel Affine Loops by Unimodular Transformations**, Parallel Computing: From Theory to Sound Practice, *IOS Press*, EWPC '92: European Workshops on Parallel Computing, Barcelona, March 1992.

Invited Papers

- [16] O'Boyle M.F.P., **MARS: A Data Parallel Approach to Distributed Shared Memory Compilation** 7th International Workshop on Compilers for Parallel Computers, Vadstena, June 1998.
- [17] O'Boyle M.F.P., **Compiler Exploitation of Relaxed Memory Consistency**, 6th International Workshop on Compilers for Parallel Computers, Aachen, December 1996.
- [18] O'Boyle M.F.P. and Knijnenberg P.M.W., **Non-Singular Data Transformations: Definition, Validity and Application** 6th International Workshop on Compilers for Parallel Computer, Julich Dec 1996.
- [19] Bodin F., Kervella L., O'Boyle M.F.P., **Synchronisation Reduction**, French Workshop on Parallel Computation, Paris , January 1995.
- [20] O'Boyle M.F.P., **Compiling Sisal for Distributed Memory Architectures**, 3rd International Workshop on Compilers for Parallel Computers Vienna, ACPC/TR 92-8, July 1992.

Technical Reports

- [21] O'Boyle M.F.P. and Knijnenberg P.M.W., **Non-Singular Data Transformations: Definition, Validity and Application** Leiden University Technical Report 96-16, June 1996.
- [22] O'Boyle M.F.P. Ford R. and Nisbet A., **Compiler Reduction of Invalidation Traffic in Shared Virtual Memory Systems**, CNC Tech Report, June 1996
- [23] Bodin F., Kervella L. and O'Boyle M.F.P. **Synchronisation Minimisation in a SPMD Execution Model**, IRISA Research Report 863, 1994.
- [24] O'Boyle M.F.P., **Compiler Reduction of Communication Overhead and its Impact on Computer Architecture**, CNC Technical Report 1993.
- [25] O'Boyle M.F.P., **A Data Partitioning Algorithm for Distributed Memory Compilation**, Tech. Rep. UMCS-93-7-1, July 1993.
- [26] Gurd J.R., Cooper M.D., Hedayat G.A., Nisbet A., O'Boyle M.F.P., Snelling D.F and Bohm A.P.W., **A Framework for Experimental Analysis of Parallel Computing** Tech. Rep. UMCS-93-2-3, February 1993.

- [27] O'Boyle M.F.P., **Program and Data Transformations for Efficient Execution on Distributed Memory Architectures** UMCS-93-1-6, January 1993.
- [28] O'Boyle M.F.P. and Nisbet A., **Shallow: A Mapping Study**, CNC Technical Report No. CNC/TR/1992/003, Department of Computer Science, University of Manchester, January 1992.
- [29] O'Boyle M.F.P. and Hedayat G.A., **Load Balancing of Parallel Affine Loops by Unimodular Transformations**, Technical Report No. UMCS-92-1-1, Department of Computer Science, University of Manchester, January 1992.