

## CV : Peter G. Hancock

### Personal data

address	7 Cluny Avenue, Edinburgh, EH10 4RN
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email	<b>hancock@spamcop.net</b>
DOB, nationality	20 December 1951, British
degrees	Department of Computer Science, University of Edinburgh, 1996-2000 Doctor of Philosophy (Oct. 2000) Queens College Oxford, 1969-1972 Double Honours (2.1) Mathematics and Philosophy

### Employment History

Nexwave Solutions, R&D Cambridge Jul 2002-Feb 2003	Senior Engineer Component based operating systems Last salary: £40,000 pa
Swansea University, Computer Science. Feb-July 2001	Fixed term Lecturer B. Developed and gave an Msc course, entitled 'Faults and Fault Tolerance'. Last salary: £24,227 pa
Digital, VMS Engineering. 1988-1995	Principal software engineer. Transaction processing, design of a queue manager, file system architecture, patent applications, formal specification, liason with universities. Last salary: £36,253 pa
Metier Management Systems Ltd. 1982-1988	Senior software engineer. Design of a message passing kernel and other system software for a database machine, board design, microcoding, diagnostics, system debugging.
Instron Ltd. 1981-1982	Software engineer. Signal processing, control engineering, systems programming.
Oxford University 1978-1981	Research assistant on psychology projects. Statistics, general programming, signal processing, systems programming.

## **Skills**

I have spent about 6 years doing research in theoretical computer science, on formal models for command-response interfaces (API's), followed by a position in an embedded systems company, where I worked mainly on TRON support.

In the previous roughly 15 years I worked as a software engineer in the computer industry. Skill summary:

**systems programming** I have usually worked on low-level systems software and control systems.

I once had a good understanding of the mathematical principles on which signal processing software is based.

I have worked on a database machine, where beside microcode for an instruction processor and a disk controller, I wrote the message-passing kernel, and a great deal of configuration and diagnostic software for a wide variety of equipment. I have used several programming languages (though not C++ or Java), and am quick to pick up new ones. I have written systems software of many kinds, and am familiar with some component architectures. I have also worked on transaction-processing support, and am familiar with fault-tolerance technology.

**Languages used:** assemblers, microassemblers, Fortran, Algol, BCPL, Bliss, Stab, Snobol, C, Haskell.

**Systems:** DEC RT11, RSX, VMS, various Unix.

**Technology areas:** control systems, control of experiments, signal processing, statistical analysis, machine architecture, simulation and measurement, machine emulation, embedded systems, component architectures, kernel programming, transaction processing support, fault tolerance, language implementation, type checking.

**administration** I have worked in a large operating systems group (DEC VMS) involving many programmers involved in several inter-related projects, and in relatively small companies with only a few programmers.

I have written project proposals, job specifications, test plans, functional specifications, formal specifications, schedules, design documents, status reports, internal and external specifications, and performance reviews. I have usually had a lead technical role.

I have lectured and in computer science, and written academic papers.

**design skills** I have shown some talent for finding simple designs to solve complicated software problems, and am proud of my contribution to some of the systems on which I have worked.

I have a strong appreciation of the importance of testing, progress tracking and maintainance in the design of a product.

**aspirations** What I would most like is a position in which I could apply what I know about the foundations of software to real systems. I am happy to do any kind of paying work of which I am capable.

## **Referees**

*(Not posted on web)*