

Prof. Michael Francis O'Boyle

Work address Informatics Forum, 10 Chricton St Edinburgh EH8 9AB
Contact mob@inf.ed.ac.uk +44 131 650 5117

Degrees

1992 Ph.D. Computer Science, U. Manchester
1990 M.Sc. (by research alone) Computer Science, U. Manchester
1987 B.Sc. Mathematics and Computer Science Aston U.

Employment

1997 - present Lecturer, Reader(2002), Professor (2006) School of Informatics, U. Edinburgh
1996 - 1996 Lecturer, Department of Computation, UMIST
1991 - 1996 Research Assistant, University of Manchester

Visiting positions and fellowships

2014 - 2015 Sabbatical: ARM Contractor Cambridge and Trondheim
2003 - 2004 Visiting Professor, Universitat Polytechnica Catalunya, Spain
2001 - 2002 Visiting Scholar, Stanford University
2001 - 2005 EPSRC Advanced Research Fellow
1997 TMR Research Fellow, Universitat Polytechnica Catalunya, Spain
1996 - present Honorary Fellow, Computer Science, University of Manchester
1996 Visiting Research Fellow, University of Vienna, Austria
1994 Visiting Research Scientist, INRIA, France
1992 - 1994 SERC Postdoctoral Research Fellow

Awards, keynotes etc.

Fellow of the British Computing Society 2010
Test of time, most influential paper award: ACM CGO 2017
Best paper: IEEE PACT 2010, CC 2011, Highest ranked paper PACT 2003
Best presentation ACM CGO 2006
HiPEAC Paper awards 2009, 2010, 2015
Keynotes at conference, industry and workshops including CATS, IPDCS, HiPEAC, ARM CC, Seri

Editorships, Programme Committees

PC Chair of ACM CGO 2011, CC 2012, HiPEAC 2009
General Chair IEEE PACT 2013, ACM PLDI 2014
Steering committee member of conferences: PACT, PLDI, CGO, CC, CPC
Editorial Board ACM Transactions on Architecture and Compiler Optimization
PC member - over 50 including CGO, ISCA, PPOPP, ASPLOS, HPCA, PACT, ICS, DSN

Research Supervision Experience

16 PhD students, 10 postdocs
Prizes won by students: 3 EPSRC/RAEng 5-year fellowships, BCS Distinguished Dissertation Award, 2 Nvidia graduate awards.

Leadership

2015-2012	Director of EPSRC CDT Pervasive parallelism
2004	Co-founder of HiPEAC
2011 -present	Director of ARM Research Centre of Excellence at Edinburgh
2012-2014	Director of Intel Honour Program, Edinburgh
2009-2014	Director of Institute of Computing Systems Architecture, U. Edinburgh
2003 onwards	External reviewer of 8 overseas programmes. Uppsals of entire CS provision
2001 -2010	Advisory board of companies: Siroyan, CAPS-Enterprise
2006-2012	Invited expert in EU computer systems and rapporteur

Research Grants

Where indicated, the value is Edinburgh's share - c. £8m currently.

2016-2019	EU H2020 Bonseyes AI market place joint-PI A Storkey c. £350,000
2016-2018	EU H2020. EuroLab4HPC PI c. £85, 000
2016-2018	EU H2020. HiPEAC4 network of excellence PI c. £215,000
2014-2021	EPSRC CDT in Pervasive Parallelism PI c. £4m
2013-2018	EPSRC PAMELA program grant Edinburgh PI, Manchester Coordinator c. £1.5m
2014-2016	EPSRC ALEA Abstraction-Level Energy Accounting Co-I c. £320,000
2013-2016	EPSRC AnyScale Applications Co-Ic. £482,000
2013-2016	EU FP7. TETRACOM PI c. £85, 000
2012-2015	EU FP7. HiPEAC3 Network of excellence PI c. £215,000
2011-2014	EPSRC PeMapp PI Predictive Modelling £580, 000.
2011-2014	EPSRC Automated Synthesis, Co-I c £992, 000.
2008-2012	EU FP7 HiPEAC2 Network of excellence. PI c. £400, 000
2006-2009	EU FP6 MilePost). Overall Coordinator. £450 000.
2006-2009	EU FET Scalable Computer Architecture (SCALA) PI £650 000
2005-2009	EPSRC Automated Synthesis Co-£874, 000.
2005-2008	EU FP6. FET Scalable Computer Architecture (SCALA) PI. £650 000
2004-2007	EU FP6. HiPEAC Network of Excellence PI c. £400, 000
2004-2007	EPSRC Compilers that Learn to Optimise (COLO) PI £299,484.
2004-2007	EPSRC Architecture and Compiler Infrastructure Co-I £283,989.
2001-2005	EPSRC Adaptive Compilation Fellowship. PI £239,646.
2002-2004	EPSRC An Integrated Hardware-Software Approach PI £167,442.
2001-2004	EPSRC Low Power Microarchitecture and Compilation PI £115,093.
2001-2004	EPSRC Software and Hardware for Efficient Speculative Multithreading Co-I £62,607.
2000-2003	EPSRC Abstraction and Performance: Co-I£62,975.
2001-2002	EPSRC A System Level Integration Approach PI£9,386.
1998-2000	EPSRC EcoVism PI , £17,337.
1998-2000	EU FP4 Mhaoteu PI £127,332.
1999-2001	EPSRC Ecole II Co-I £169,991.
1996-1999	EC FIV Oceans Co-I .c. £100,000.
1996-1999	EC FIII SODA Co-I c. £100,000.
1992-1994	SERC Automatic Parallelisation: Fellowship. Principal Investigator. c. £50,000.
1992-1995	SERC Recurrence For-Loop Extensions (Reflex): £139,059